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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Richard I

Richard L. Coulson et al.

Group Art Unit:

2863

Serial No.:

10/660,310

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Examiner:

Aditya S. Bhat

Filed:

September 11, 2003

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For:

Adaptive Cache Algorithm For

Atty. Dkt. No.:

ITL.1029US

Temperature Sensitive Memory

(P16765)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated December 22, 2004 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.1029US).

Respectfully submitted,

January 6, 2005

Date

Timothy N. Trop, Reg. No. 28,994

Trop, Pruner & Hu, P.C.

8554 Katy Freeway, Ste. 100

Houston, TX 77024

713/468-8880

713/468-8883 [fax]

Customer No.

21906

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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450.

Monica Jacobs

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					ATTY DOCKET NO. ITL, 1029US (P16765	SERIAL NO. 10/660,310			
					APPLICANT(S):				
					Richard L. Coulson et al.				
					FILING DATE: GROUP ART UNIT:				
					September 11, 2003 2863				
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